

AUG 28 2007

REMARKS/ARGUMENTS

Applicant gratefully acknowledges the indication that claims 10-34 are allowed. In light of the above amendments and the remarks below, it is respectfully submitted that all pending claims are now in condition for allowance.

Pending claim 1 stands rejected under 35 U.S.C. § 102(e) over U.S. Publication No. 2003/0199257 (Wilkinson). Applicant respectfully traverses the rejection. As to Wilkinson, the Office Action contends that input sampling coupler 200a is the recited first terminal and distortion sampling coupler 200b is the second terminal. Furthermore, while the Office Action contends that Wilkinson teaches the recited third terminal to couple to an output of a single-ended source, there is no support in the Office Action or Wilkinson for this contention. That is, as to amended claim 1, both the first terminal and the third terminal are to couple to the same output, namely an output of a single-ended signal source. Obviously the Office Action must contend that signal source 251 is the single-ended signal source, the output of which is provided to input sampling coupler 200a. However, this is the only point at which this signal source is coupled. Thus Wilkinson fails to teach the recited third terminal. In this regard, the first delay line 240 which is coupled to an output of the input sampling coupler 200a cannot be considered the third terminal as it is not coupled to the signal source 251.

Furthermore, Wilkinson fails to teach the two recited loops, the first of which comprises the first and second terminals and the second of which comprises the second and third terminals. In this regard, as there is no third terminal as set forth in claim 1, there cannot be a second loop that comprises this third terminal. For at least these reasons, claim 1 and the claims depending therefrom are patentable over the cited art.

New independent claim 49 and the claims depending therefrom are patentable as the cited art nowhere teaches or suggests multiple loops that are arranged so that a first interfering signal induced in the first loop by an interference source is cancelled by a second interfering signal induced in the second loop by the interference source. Accordingly, new independent claim 49 and the claims depending therefrom are patentable over the cited art.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

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